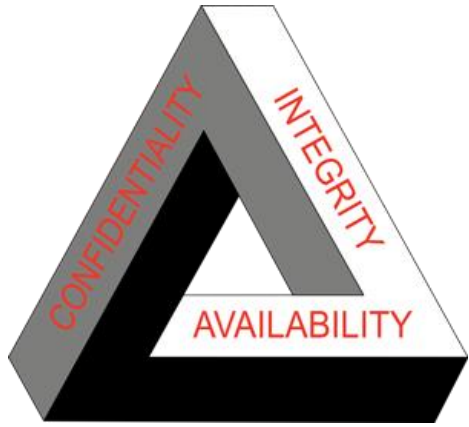


Future-Proof Software-Systems (FPSS)

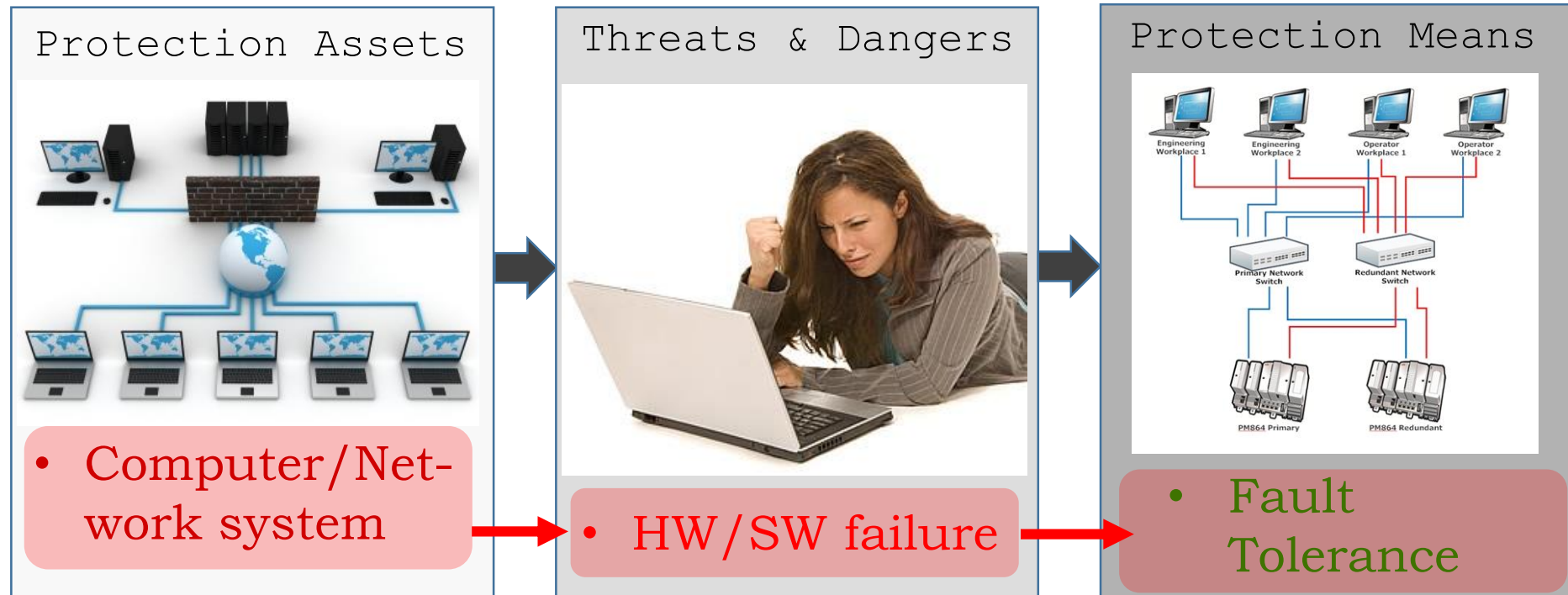
Part 4B: Architecting for Dependability

Lecture WS 2019/20: Prof. Dr. Frank J. Furrer



Dependability Property:
Availability

Availability



Availability

Percentage of time a computer system's information and functionality is *ready* for the intended use.

The math behind availability:

$$\text{Availability} = \frac{\text{Uptime}}{\text{Uptime} + \text{Downtime}} \quad [\%]$$



The math behind availability:

$$\text{Availability} = \frac{\text{Uptime}}{\text{Uptime} + \text{Downtime}} \quad [\%]$$

Example:

Uptime per day: 23.9 hours = **1'434 min**

Downtime per day: 0.1 hours = **6 min**

$$\text{Availability} = 1'434 / (1'434 + 6) = 0.99583$$

[99,583 %]

99.999 %

99.99 %

99 %

99.9 %

99.9999 %

The «9» notation: **«Three nines»**

Availability Techniques:

Technology:

- Redundancy: Standby/switchover (hot/cold standby)
- Monitoring: early failure detection
- Fallback: Revert to old software release
- Reroute/Network reconfiguration
- Degraded operation

Processes:

- Planned downtimes (Sunday 02:00 – 02:30)
- Fast human intervention

Example:

∅ -50%/+400%

> 1'000 changes/day

~ 10 disruptions/h

Business Load

Intended Changes

Disruptions



60'000 Servers



2'000 Routers

10'000 Business Databases



12'000 Business Applications

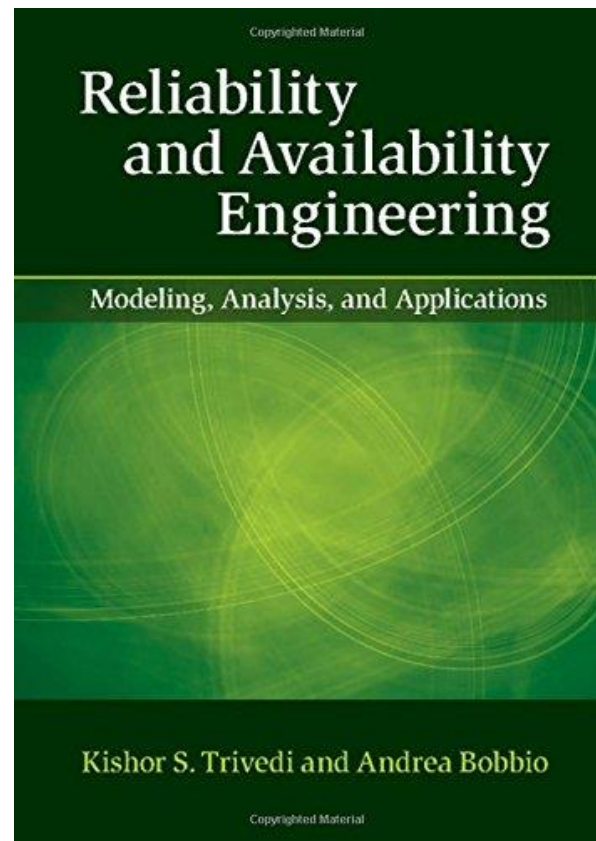


90'000 Workstations

Large Computing Infrastructure

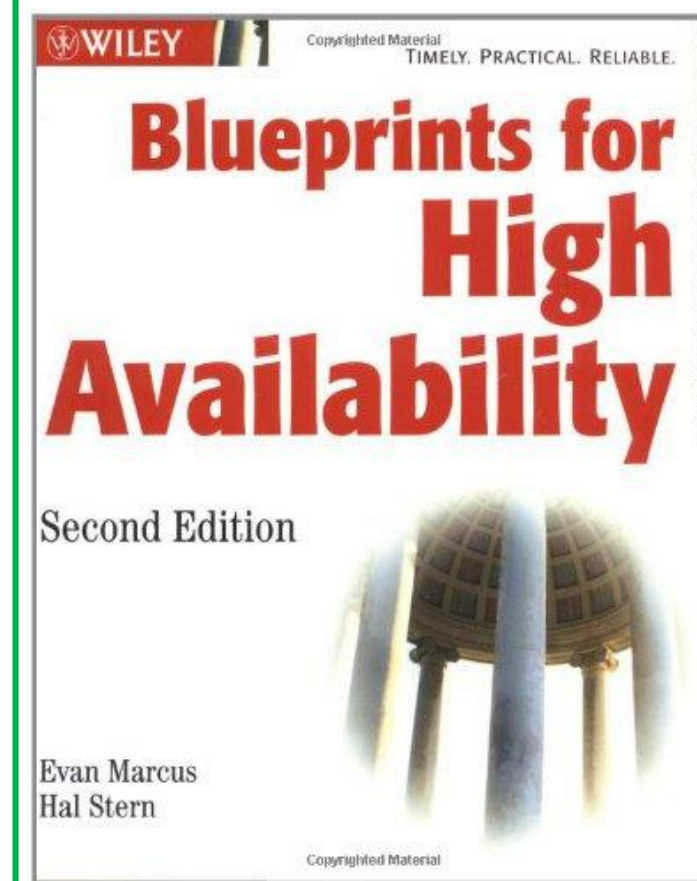
Required Availability:
99.9 %
NOT including planned downtime

Textbook



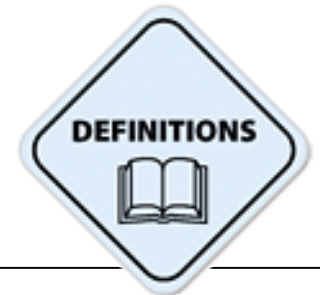
Kishor S. Trivedi, Andrea Bobbio:
**Reliability and Availability Engineering:
 Modeling, Analysis, and Applications**
 Cambridge University, 2017. ISBN 978-1-107-
 09950-0

Textbook



Evan Marcus, Hal Stern:
Blueprints for High Availability
 John Wiley & Sons, USA, 2nd edition, 2003. ISBN
 978-0-471-43026-1

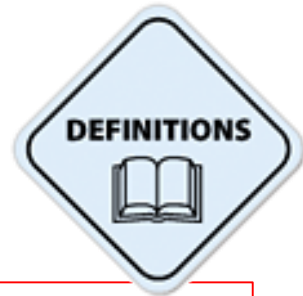
Dependability Property:
Real-Time Capability



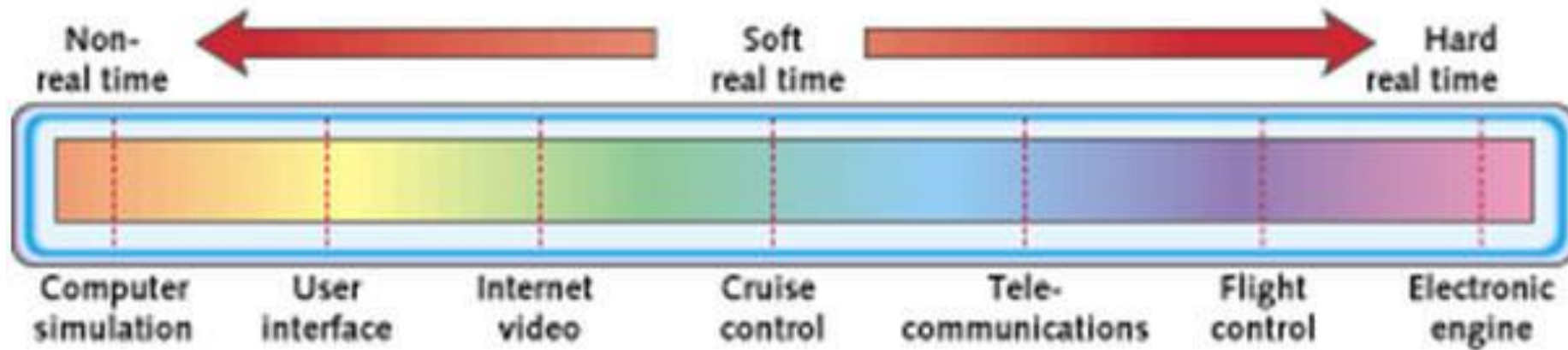
Real-time computing (RTC), or reactive computing describes hardware and software systems subject to a ***real-time constraint***, for example from event to system response.

Real-time systems must guarantee response within specified time constraints, often referred to as ***deadlines***

https://en.wikipedia.org/wiki/Real-time_computing

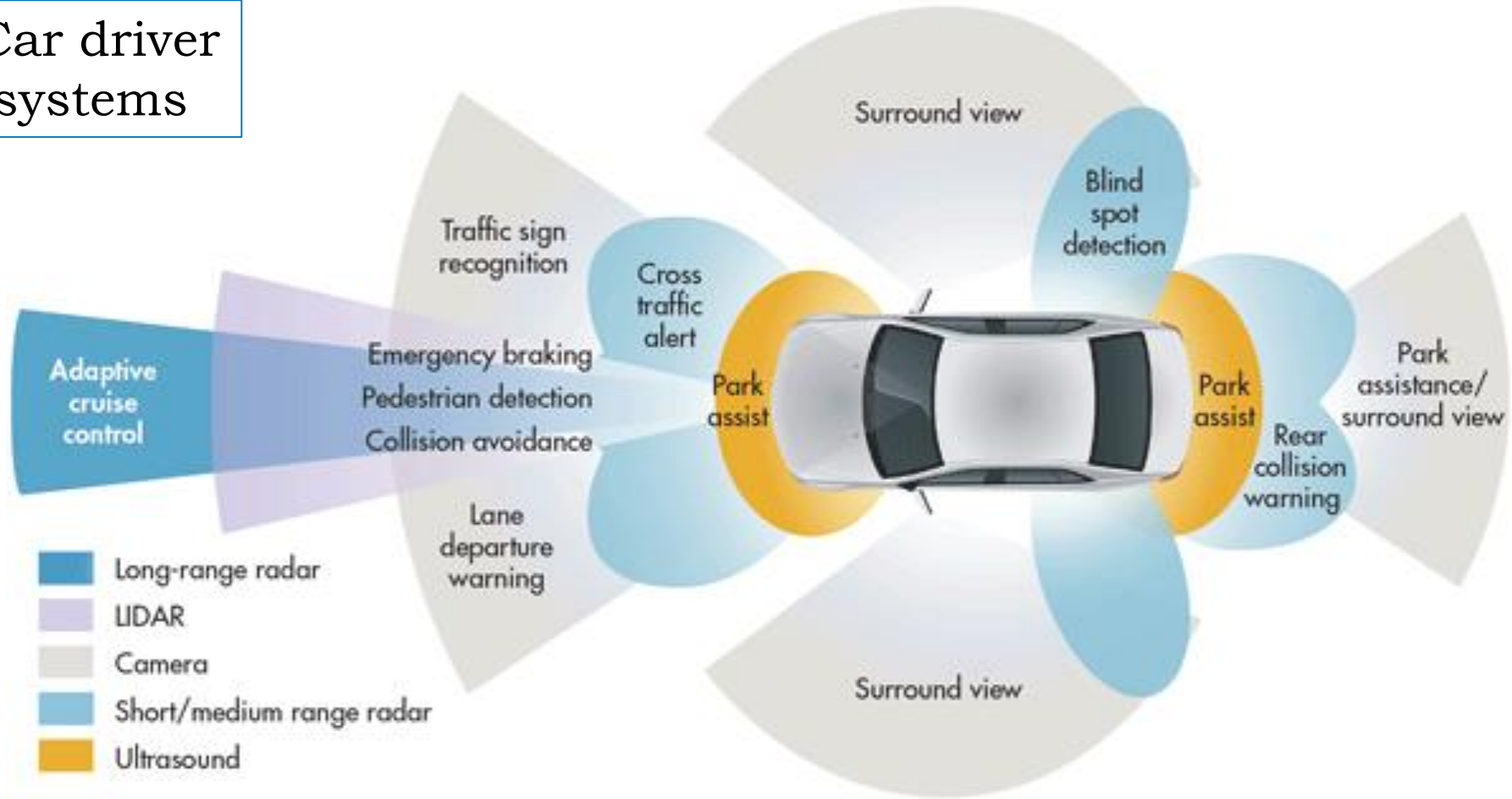


Real-time capability is the capability to react to events in a *predictable*, *guaranteed* time



Example: Car driver assistance systems

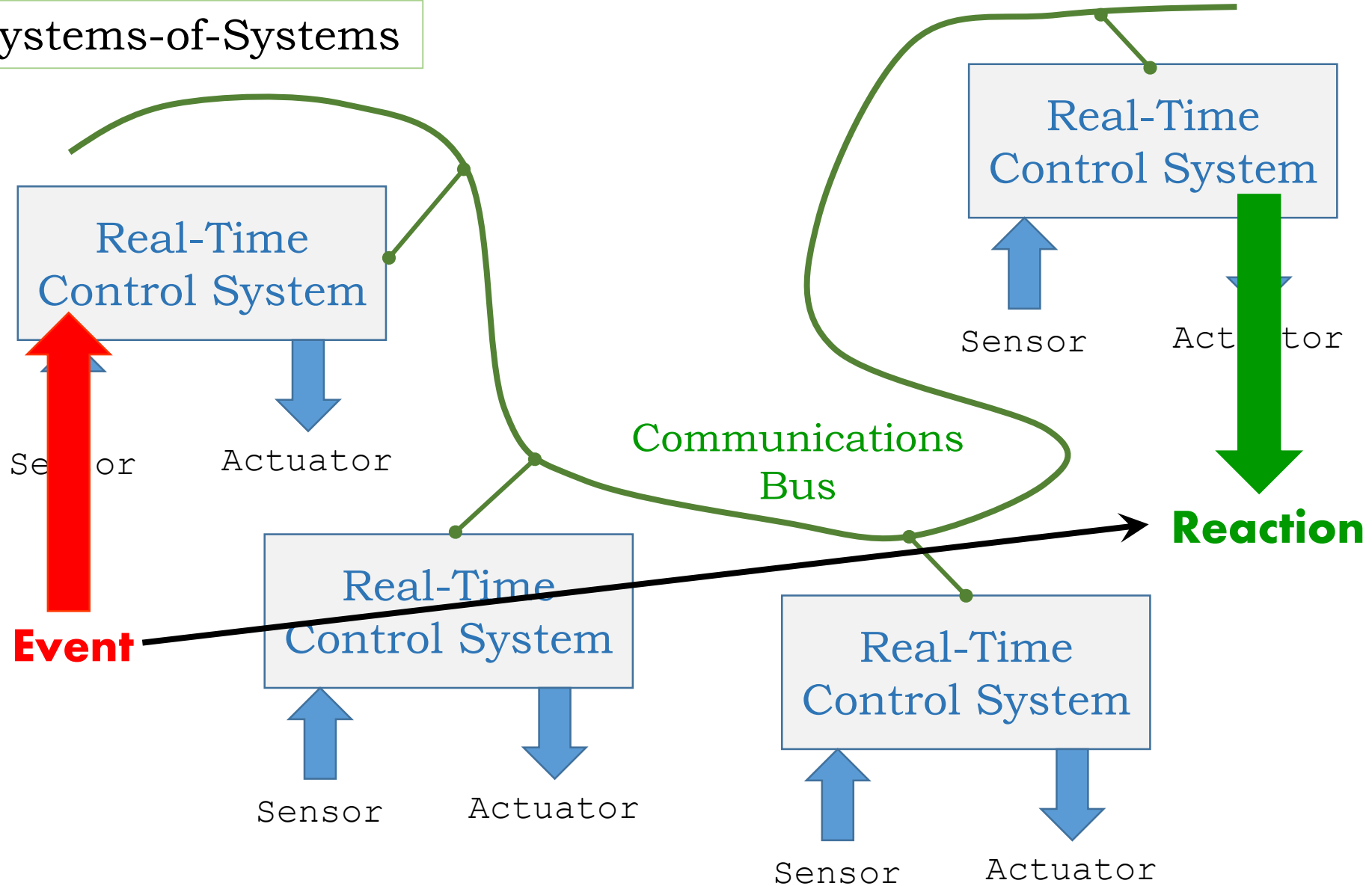
<http://electronicdesign.com>



Data acquisition ⇒ processing ⇒ event ⇒ decision ⇒ reaction

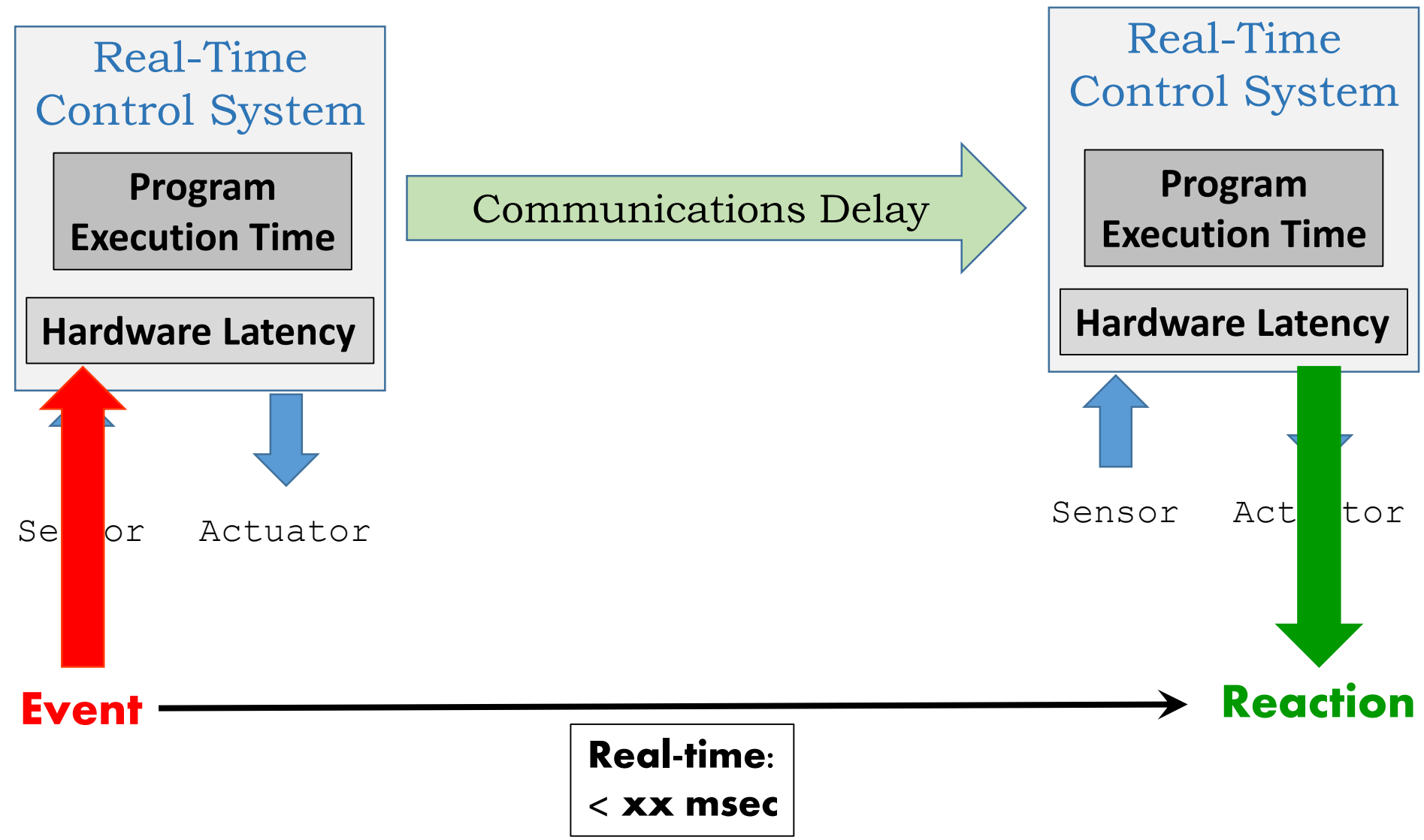
Real-time: max. xx msec

Real-world: Systems-of-Systems

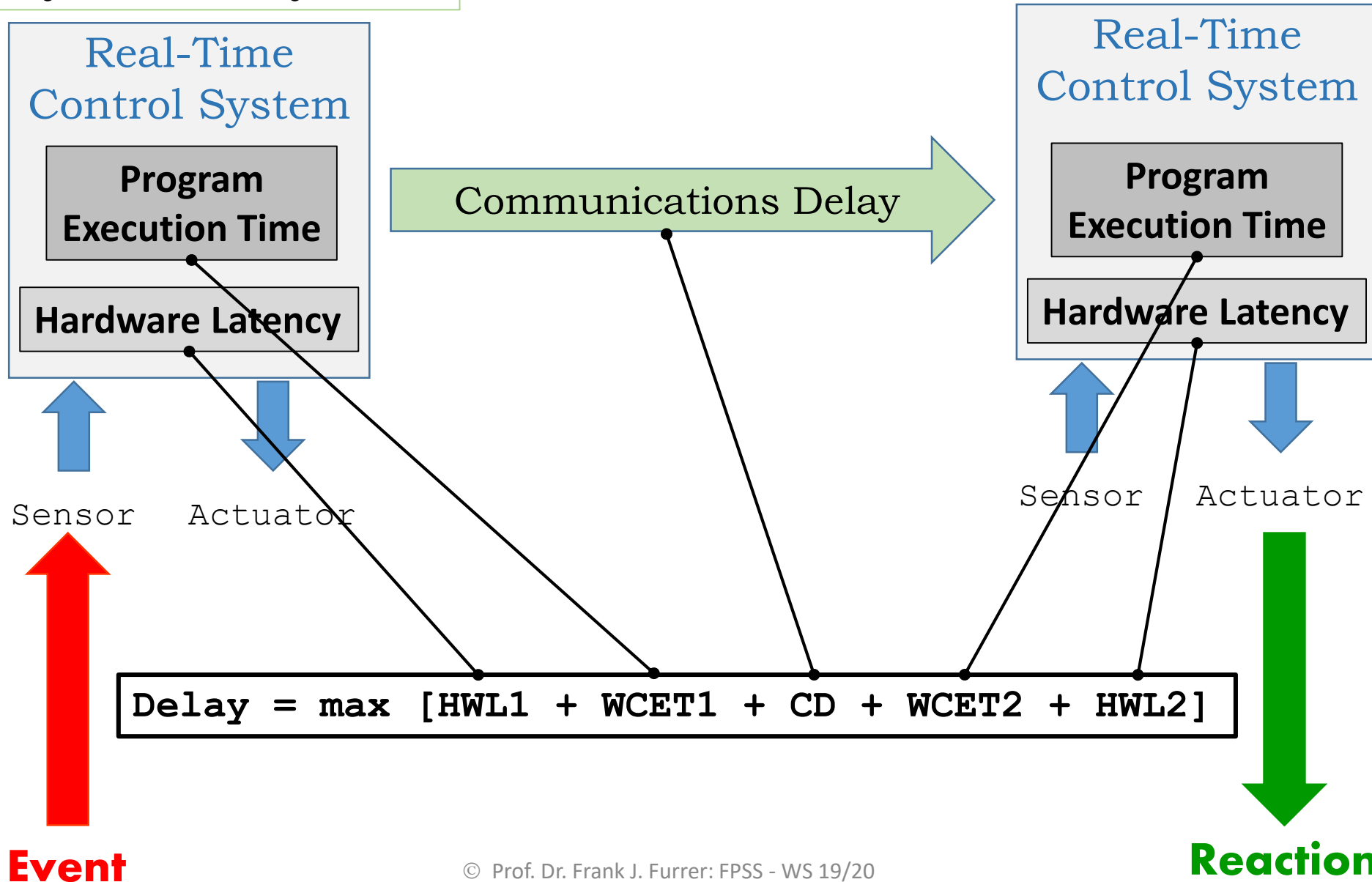


Real-time:
 $< \mathbf{xx \text{ msec}}$

Real-world: Systems-of-Systems



Real-world: Systems-of-Systems



Real-world: Systems-of-Systems

$$\text{Delay} = \max [\text{HWL1} + \text{WCET1} + \text{CD} + \text{WCET2} + \text{HWL2}]$$

Communication Delay:
Max: xx sec
Exact: xx sec

WCET:
Worst Case Execution Time
Max: xx sec

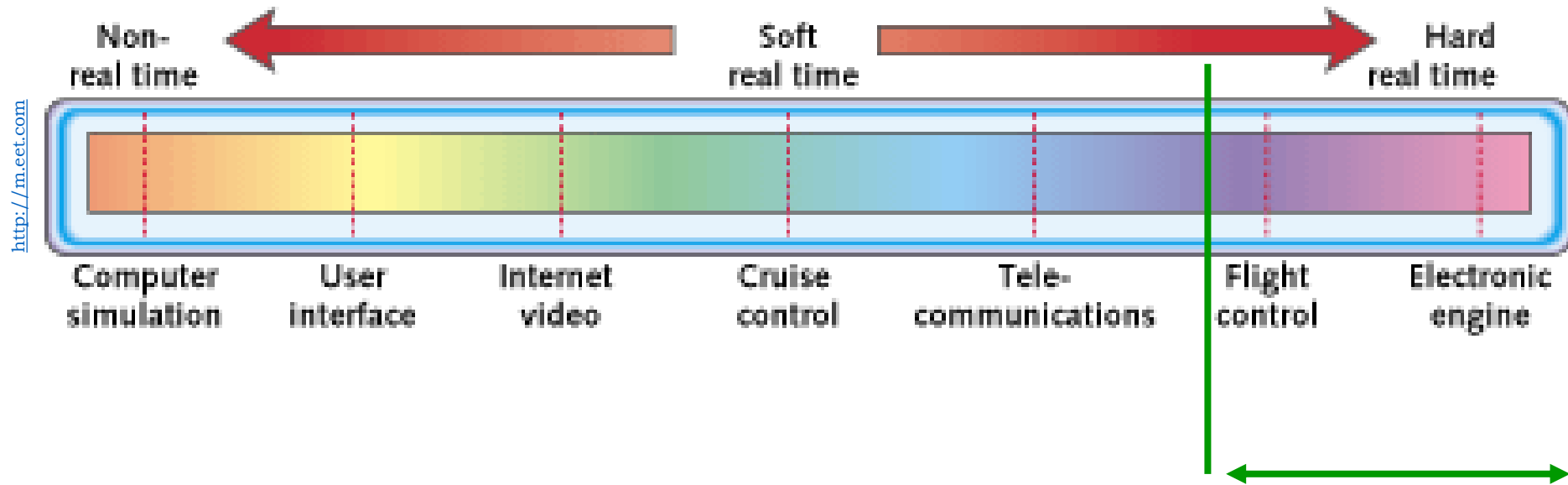
Hardware Latency Time:
Max: xx sec

Extremely **difficult** to estimate and guarantee



Real-world: Systems-of-Systems

$$\text{Delay} = \max [\text{HWL1} + \text{WCET1} + \text{CD} + \text{WCET2} + \text{HWL2}]$$

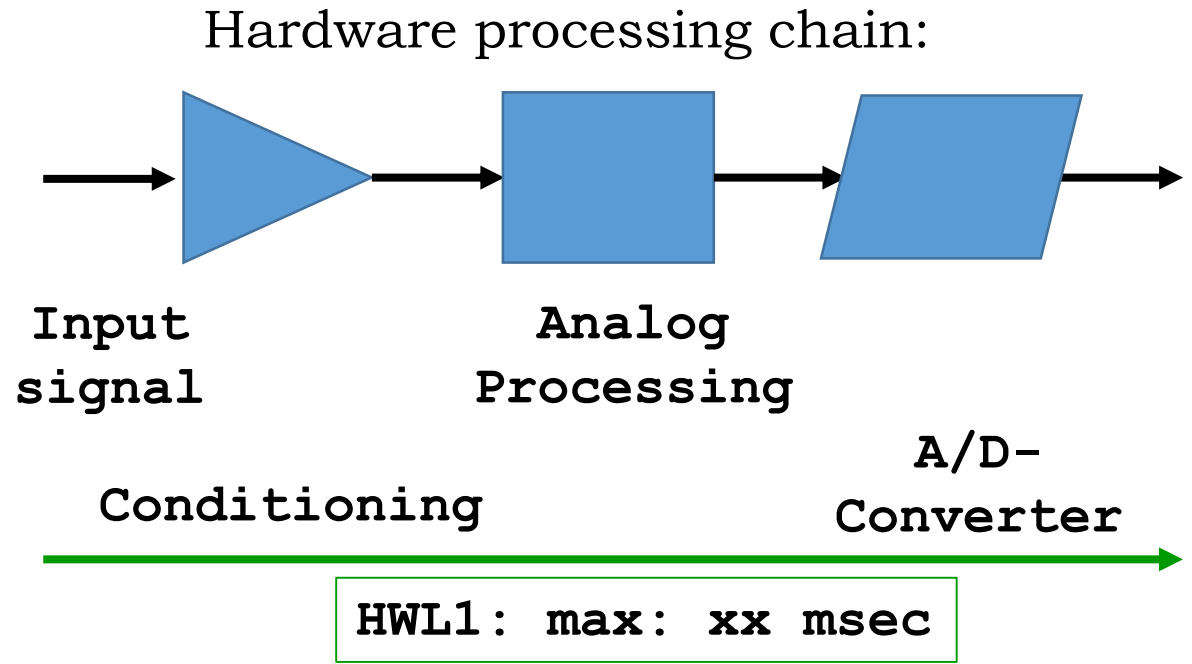
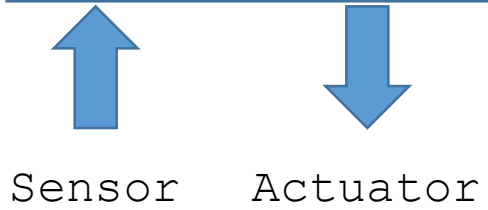
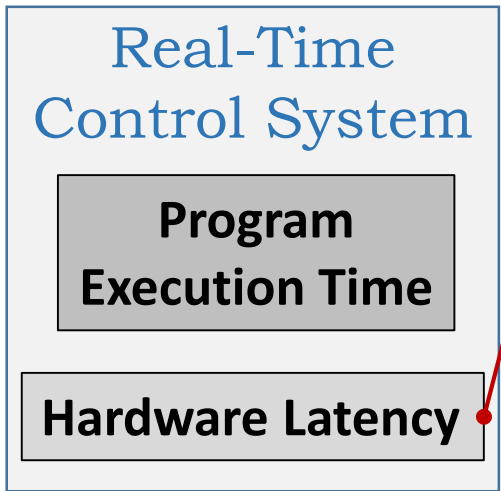


Real-time systems must guarantee response within specified time constraints ***under all operating conditions***

Safety-critical systems

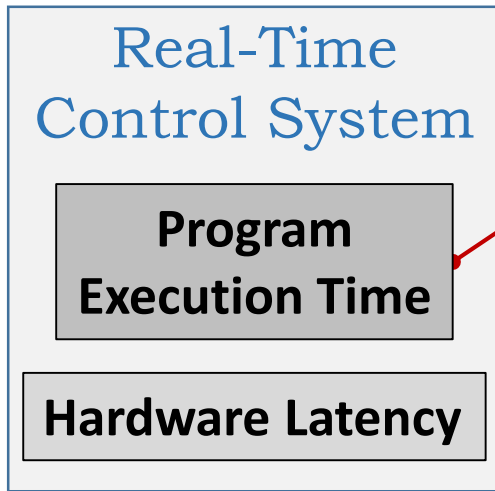
Real-world: Systems-of-Systems

$$\text{Delay} = \max [\text{HWL1} + \text{WCET1} + \text{CD} + \text{WCET2} + \text{HWL2}]$$

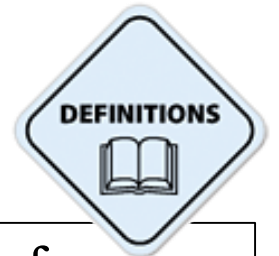


Real-world: Systems-of-Systems

$$\text{Delay} = \max [\text{HWL1} + \text{WCET1} + \text{CD} + \text{WCET2} + \text{HWL2}]$$



WCET: Worst Case Program Execution Time



The worst-case execution time (**WCET**) of a computational task is the **maximum length of time** the task could take to execute on a specific hardware platform



Real-world: Systems-of-Systems

$$\text{Delay} = \max [\text{HWL1} + \text{WCET1} + \text{CD} + \text{WCET2} + \text{HWL2}]$$

Program Execution Time

WCET: Worst Case Program Execution Time

<https://www.cs.uic.edu>

```

#include <stdio.h>
#include <stdlib.h>
#include <string.h>
#include <fcntl.h>
#include <sys/shm.h>
#include <sys/stat.h>

int main()
{
    /* the size (in bytes) of shared memory object */
    const int SIZE 4096;
    /* name of the shared memory object */
    const char *name = "OS";
    /* strings written to shared memory */
    const char *message_0 = "Hello";
    const char *message_1 = "World!";

    /* shared memory file descriptor */
    int shm_fd;
    /* pointer to shared memory object */
    void *ptr;

    /* create the shared memory object */
    shm_fd = shm_open(name, O_CREAT | O_RDWR, 0666);

    /* configure the size of the shared memory object */
    ftruncate(shm_fd, SIZE);

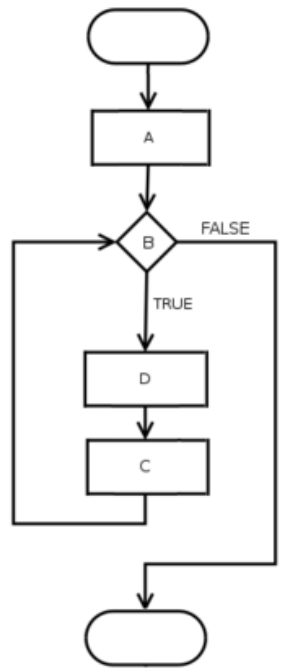
    /* memory map the shared memory object */
    ptr = mmap(0, SIZE, PROT_WRITE, MAP_SHARED, shm_fd, 0);

    /* write to the shared memory object */
    sprintf(ptr,"%s",message_0);
    ptr += strlen(message_0);
    sprintf(ptr,"%s",message_1);
    ptr += strlen(message_1);

    return 0;
}

```

for(A;B;C)
D;



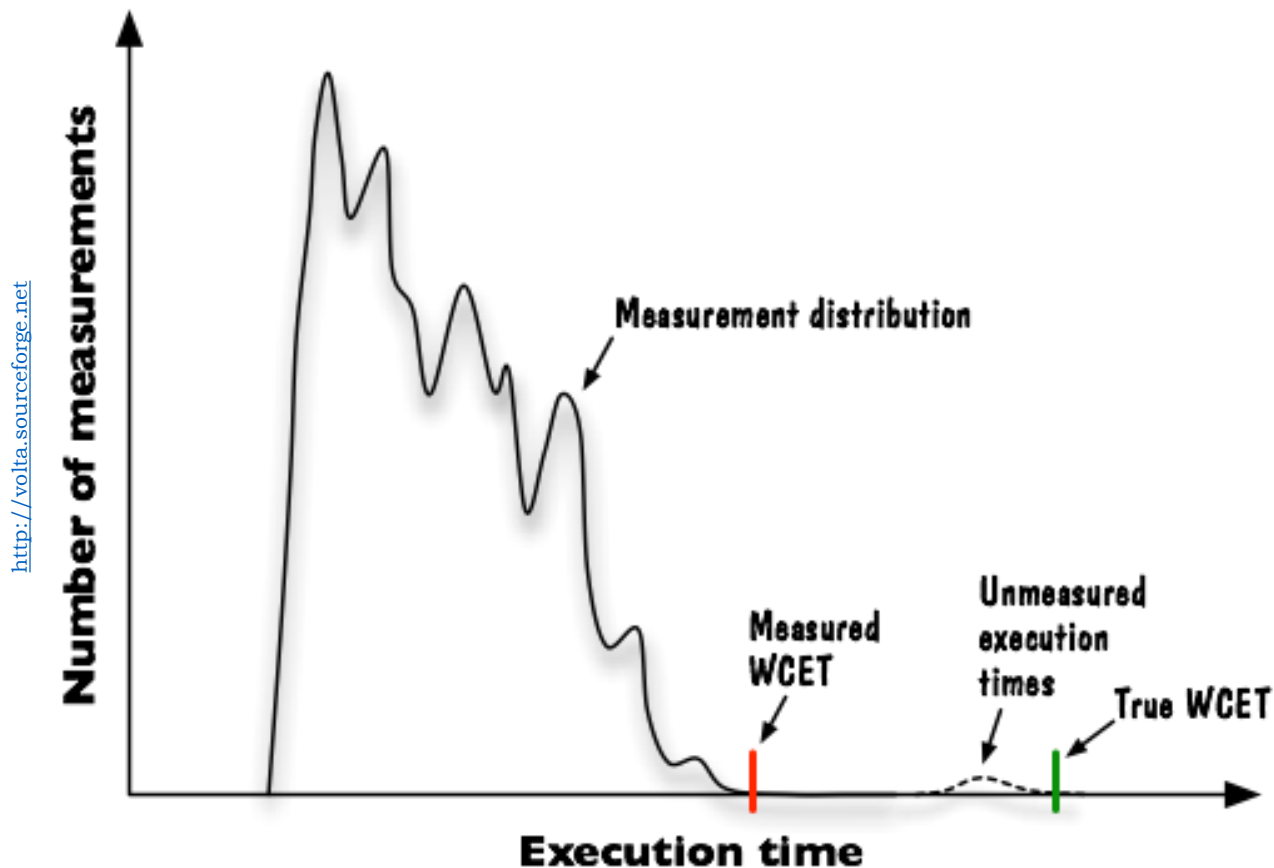
WCET:
Longest
possible path
through the
program
⇒ msec_{max}

Figure 3.17 Producer process illustrating POSIX shared-memory API.

$$\text{Delay} = \max [\text{HWL1} + \text{WCET1} + \text{CD} + \text{WCET2} + \text{HWL2}]$$

**Program
Execution Time**

WCET: Worst Case Program Execution Time



Many methods & tools for the WCET determination exist
 ⇒ Very important parameter for hard real-time systems!

Real-world: Systems-of-Systems

$$\text{Delay} = \max [\text{HWL1} + \text{WCET1} + \text{CD} + \text{WCET2} + \text{HWL2}]$$

Communications Delay

Communication Delay:

Max: xx sec

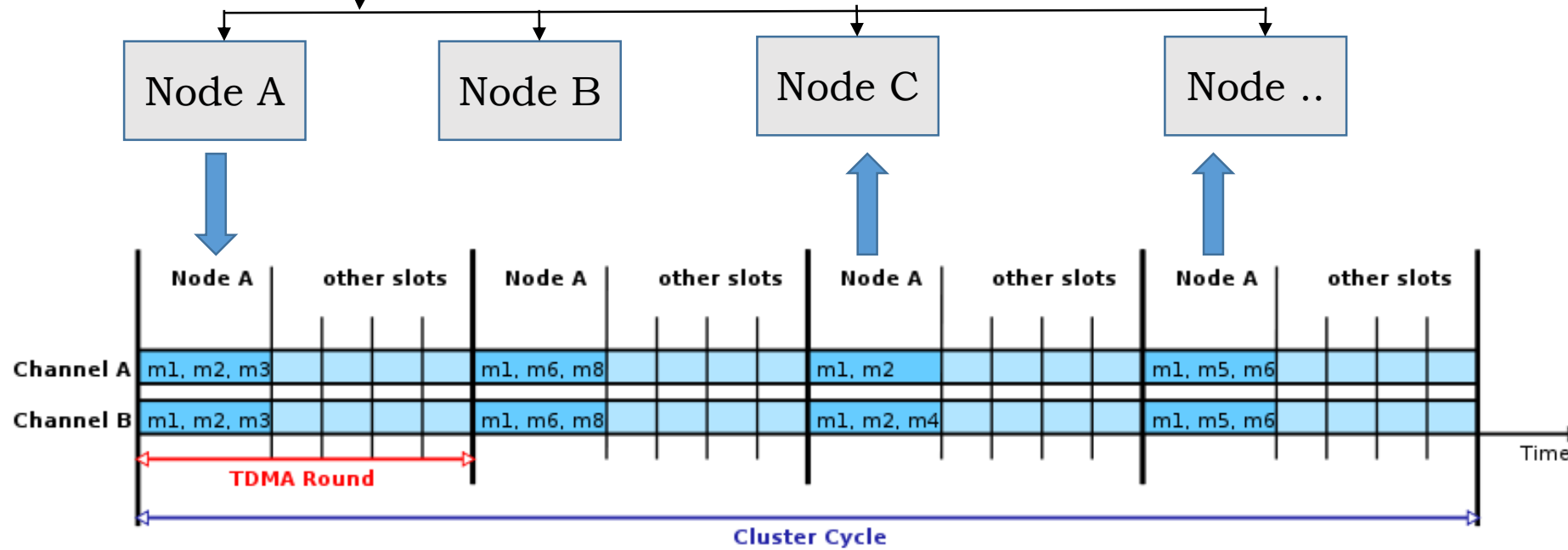
Exact: xx sec

Real-time bus: e.g. TTA, Flexray

Example: Real-time Bus (Time-Triggered Architecture **TTA**)



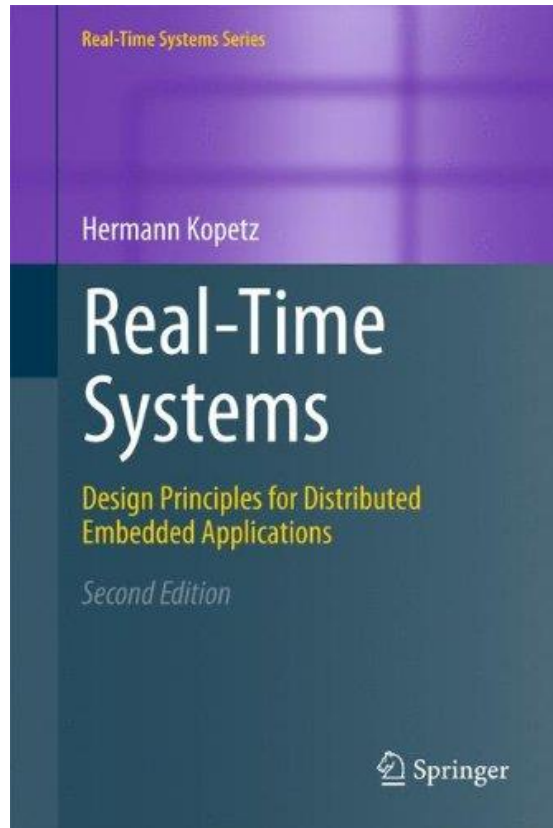
Master Clock:
Clock Synchronization Algorithm



Guaranteed real-time behaviour of the channel

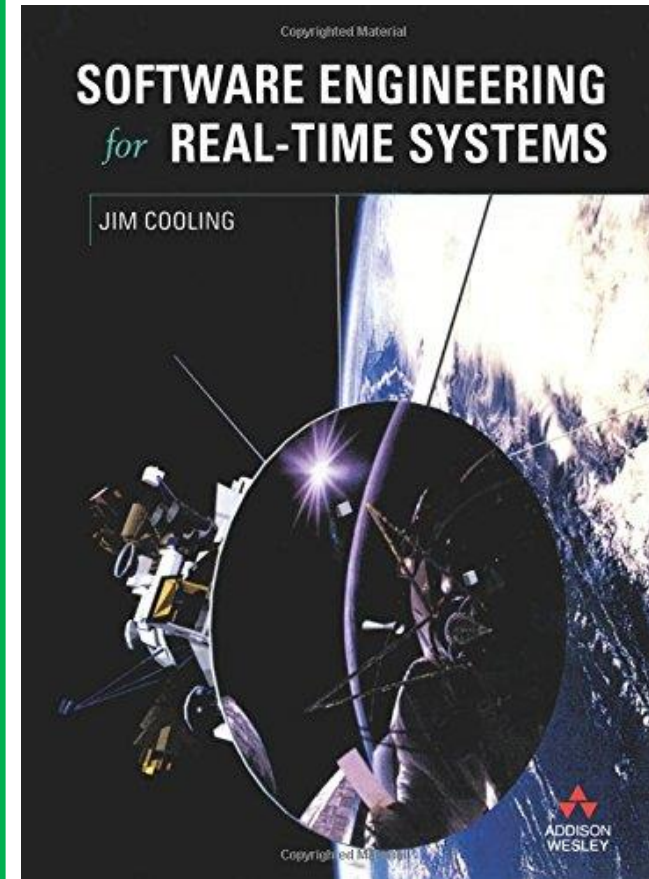
The messages are transported in exactly defined and assigned time slots, based on precise clock synchronization in all nodes

Textbook



Hermann Kopetz:
Real-Time Systems: Design Principles for Distributed Embedded Applications
 Springer-Verlag, 2nd edition, 2011. ISBN 978-1-441-98236-0

Textbook



Jim Cooling
Software Engineering for Real-Time Systems
 Addison Wesley, USA, 2002. ISBN 978-0-201-59620-5

Dependability Engineering and Methodology

- New project
- System extension



Architecture team

Changeability architecting

Dependability architecting

Development,
Implementation,
Deployment, Operation

Principles
Patterns
Frameworks

Consistency assurance, quality checking, validation, verification

Dependability Methodology



Methodology:

A system of *principles* and *rules* from which specific methods or procedures may be derived to interpret or solve different problems within the scope of a **particular discipline**

Note: *Unlike an algorithm, a methodology is not a formula but a set of practices.*

<http://www.businessdictionary.com>

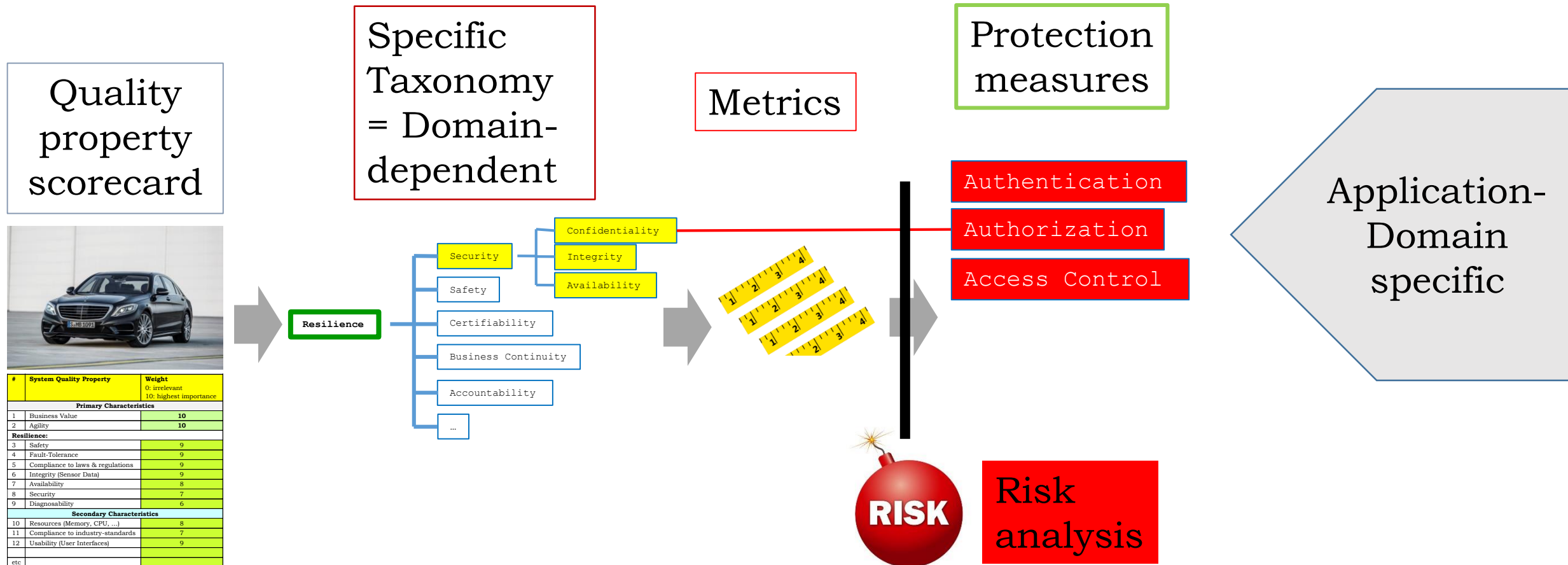


Particular discipline

= **Building dependable systems**

Dependability Methodology

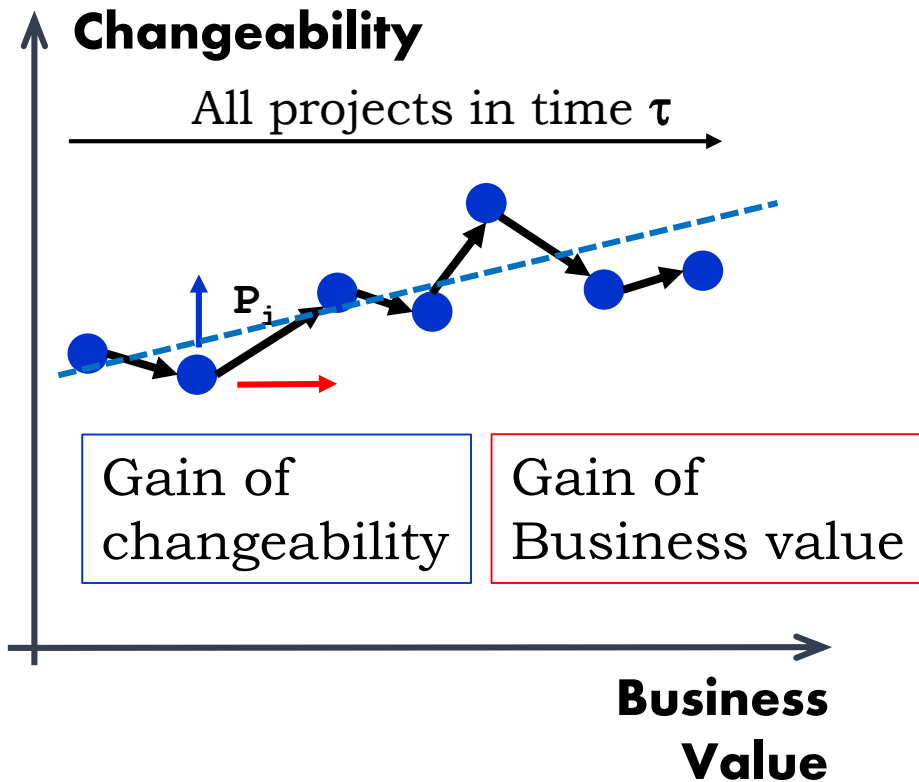
Part 1: Dependability Taxonomy



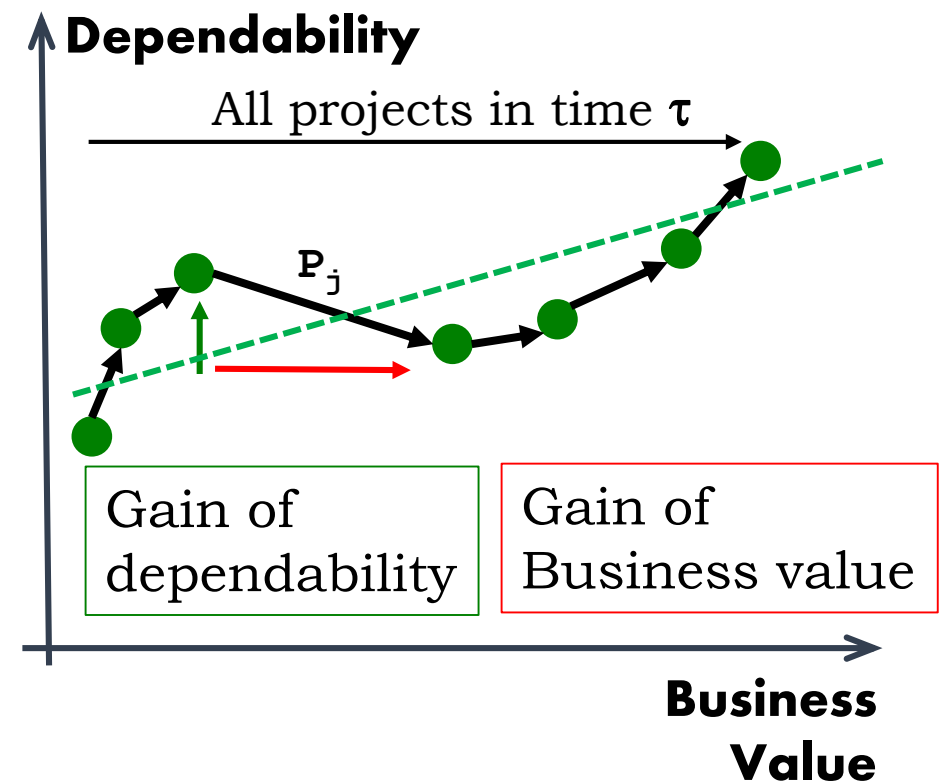
Dependability Methodology

Part 2: Dependability Strategy

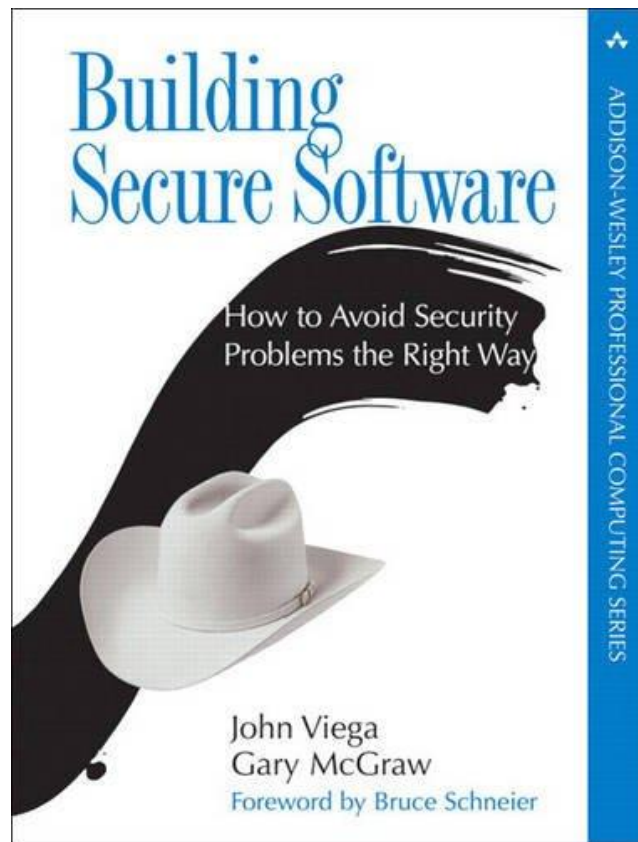
Changeability Evolution Trajectory



Dependability Evolution Trajectory

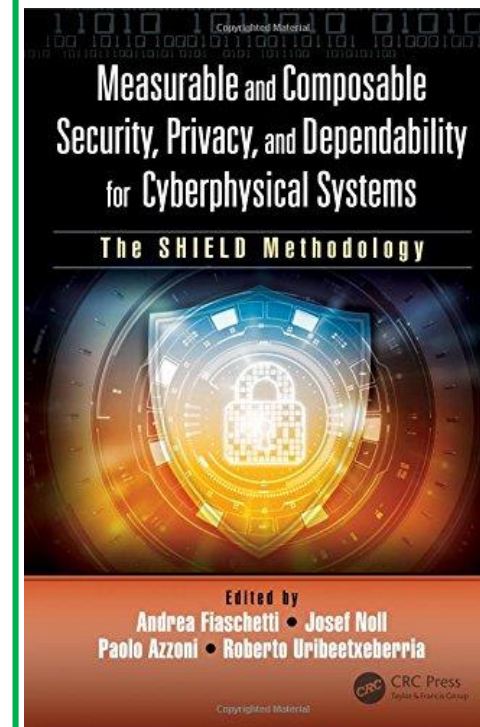


Textbook



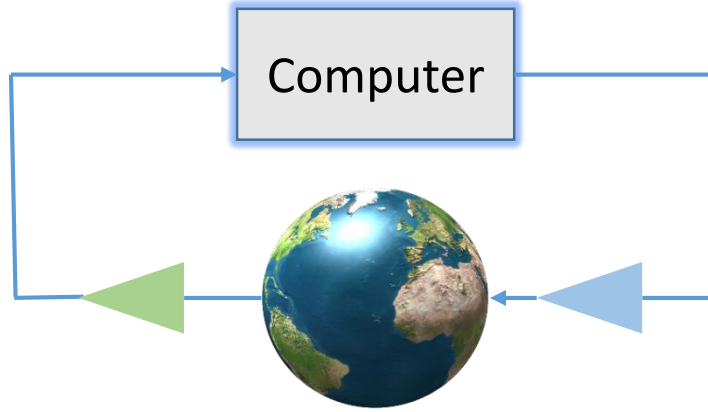
John Viega, Gary R. McGraw:
Building Secure Software: *How to Avoid Security Problems the Right Way*
 Addison-Wesley Educational Publishers Inc,
 USA, 2006. ISBN 978-0-321-42523-2

Textbook



Andrea Fiaschetti, Josef Noll, Paolo Azzoni,
 Roberto Uribeetxeberria:
**Measurable and Composable Security,
 Privacy, and Dependability for Cyberphysical
 Systems: *The Shield Methodology***
 CRC Press, Taylor & Francis, USA, 2018. ISBN
 978-1-138-04275-9

Risk Management



Fault, Failure



Attack, Intrusion

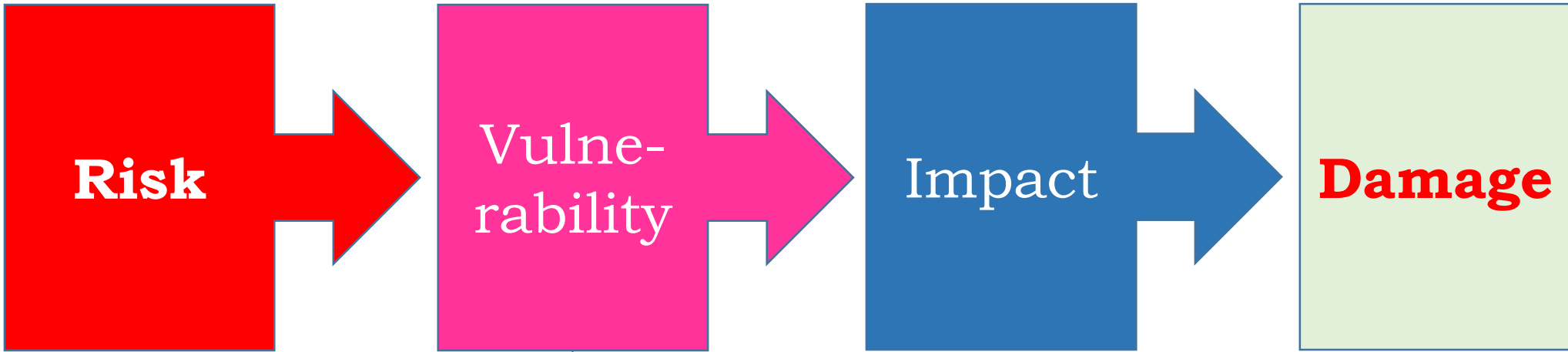
Risk
= Inherent **property** of cyber-physical systems



Risk Management
= Decisive part of systems engineering



Risk Management Basics



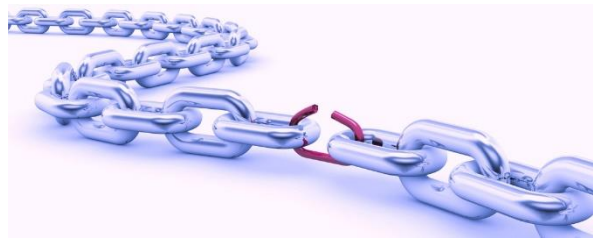
Unsafe flight condition

Threat

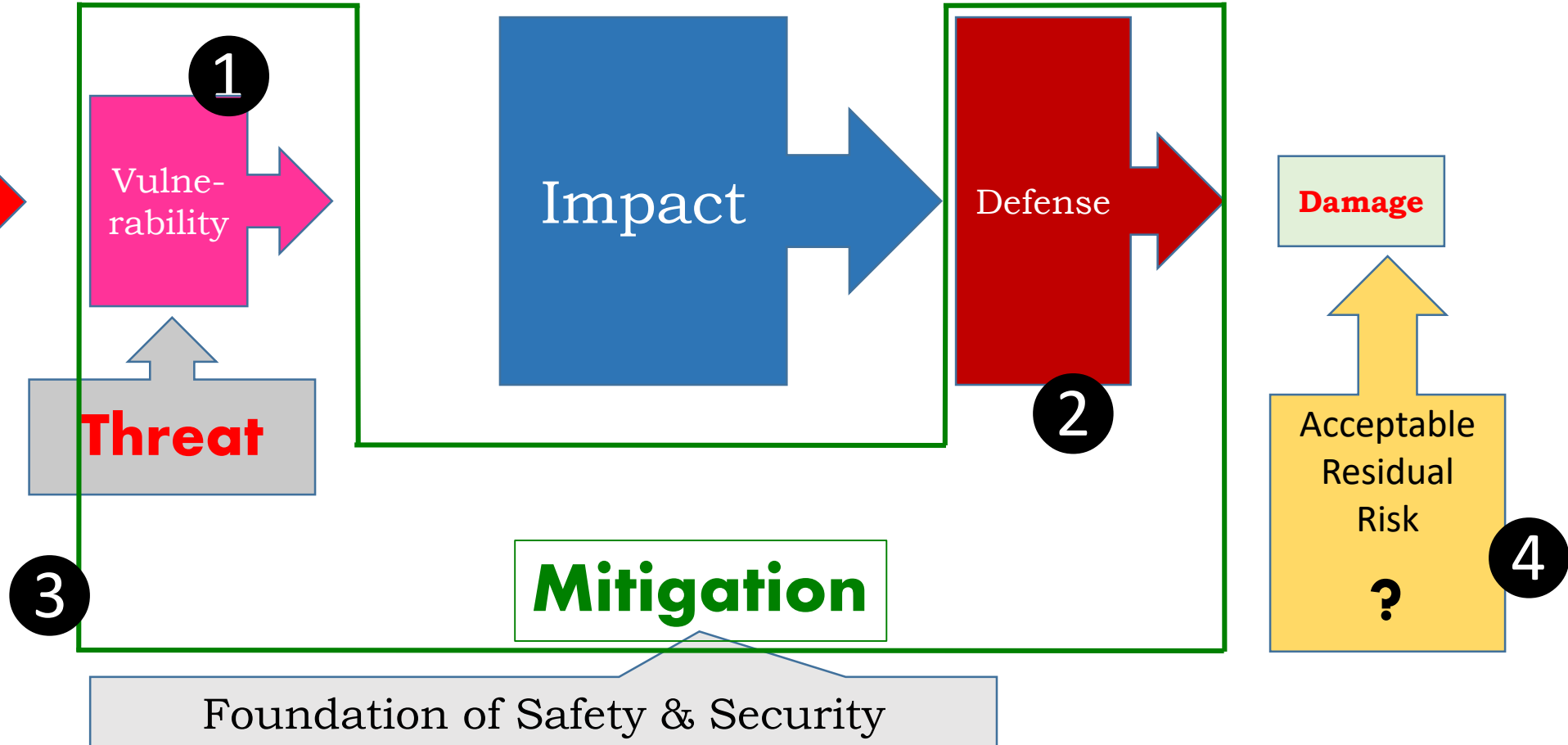
stalling

MCAS
Maneuvering Characteristics Augmentation System
overrules pilots

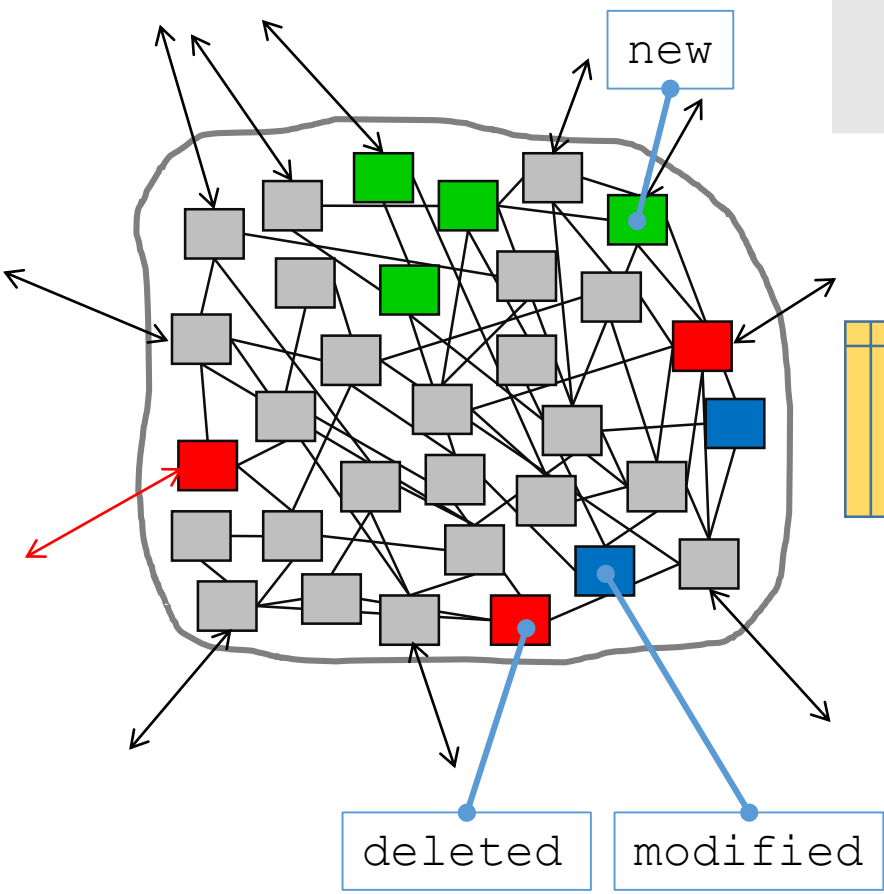
crash



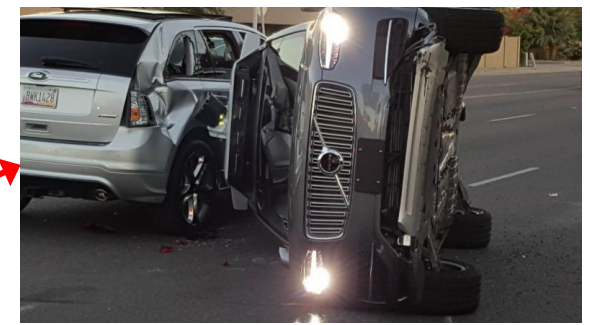
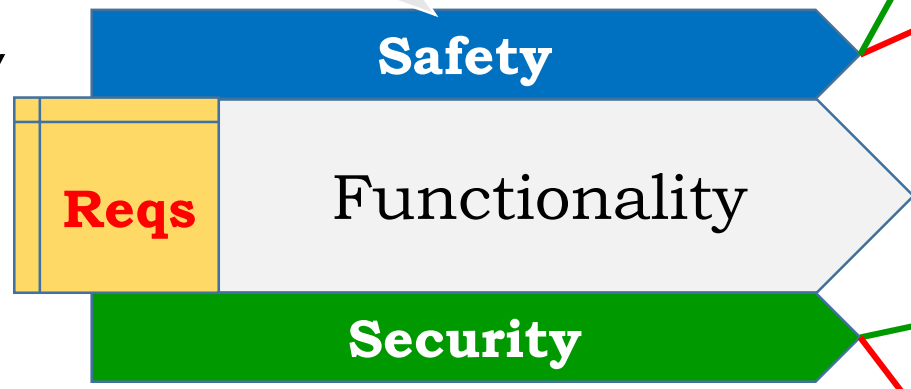
Risk Management Basics



System extension
→ Project



Safety and Security concerns/requirements have higher priority than functionality



SiteCheck Results Website Details Blacklist Status

Warning: Malicious Code Detected on This Website!

Website: [redacted]
 Status: **Infected With SEO Spam. Immediate Action is Required.**
 Web Trust: **Not Currently Blacklisted (10 Blacklists Checked)**

Scan	Result	Severity	Recommendation
Malware	Detected	Critical	GET YOUR SITE CLEANED



Identification
of
Risks

Search
for
Vulnerabilities

Predict
Impact

Assess
Damage
& Probability

**Miti-
ga-
tion,**

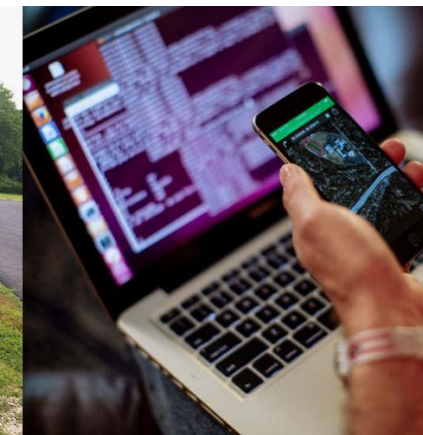
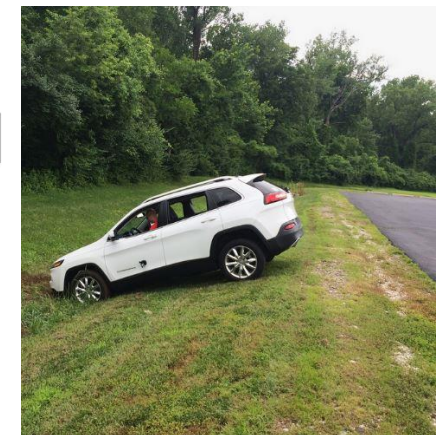
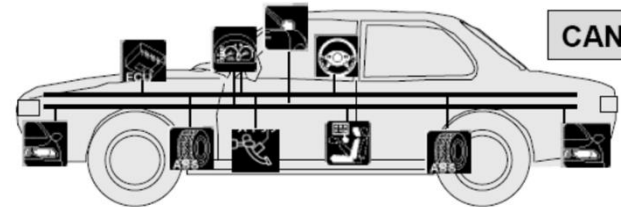
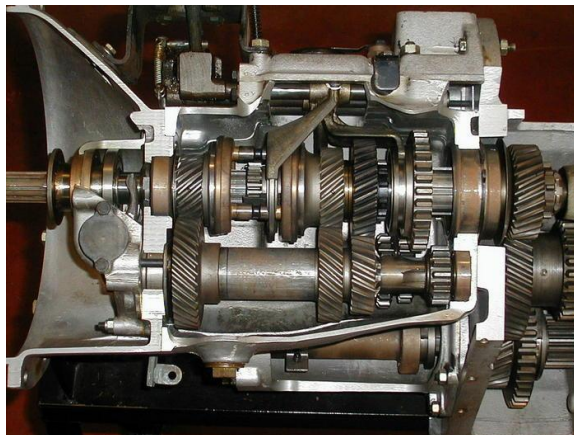
**Pro-
tec-
tion**

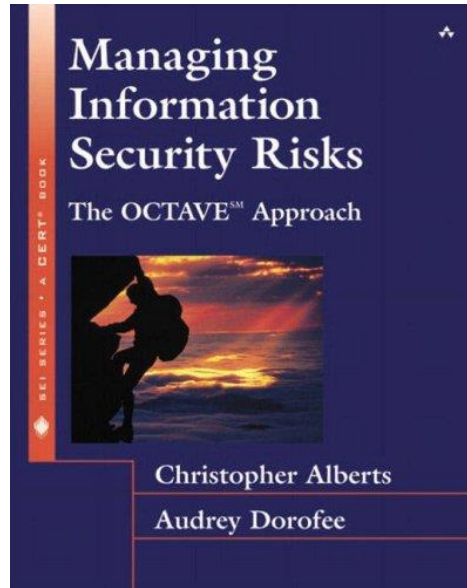
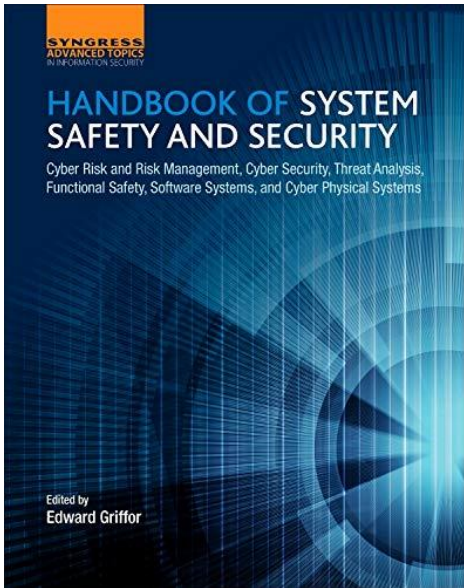
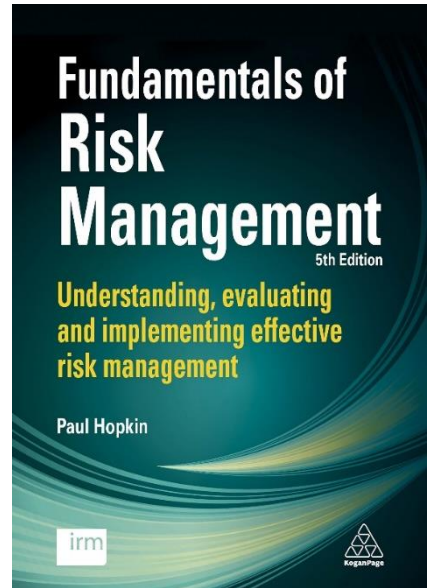
Risk

Vulne-
rability

Impact

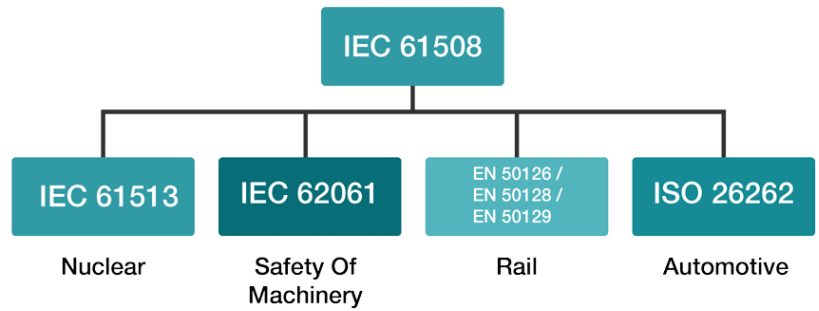
Damage





- A significant number of **risk management methodologies** exist
- Many industries are based on risk management **standards**
- Companies have their own set of **methodologies & standards**

ISO 26262
Road Vehicles - Functional Safety



<http://inadinaofset.com>

<http://www.clker.com>

<http://onthejob.45things.com>

<https://www.123rf.com>



**Identified
(known)
Risks**

**Hidden
(unknown)
Risks**

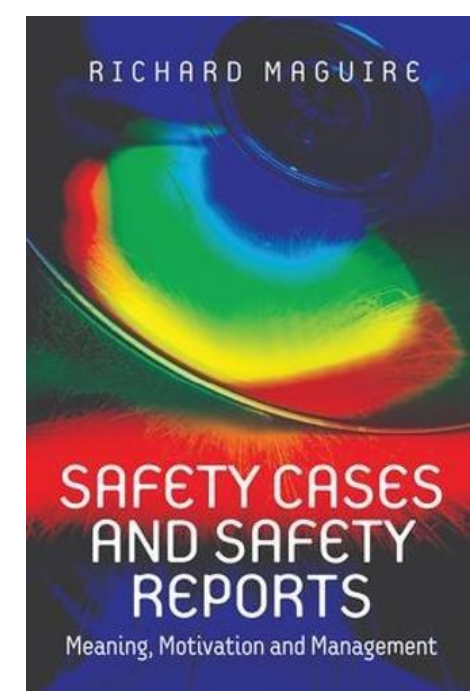
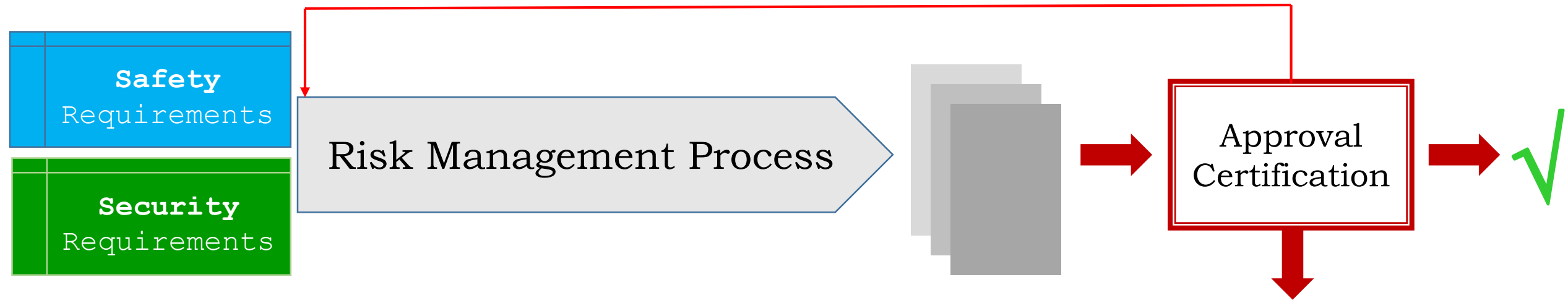


- ✓ Mitigation
- ✓ Protection

✓ Generic Protection Measures

- ❖ Safety Engineer
- ❖ Security Engineer



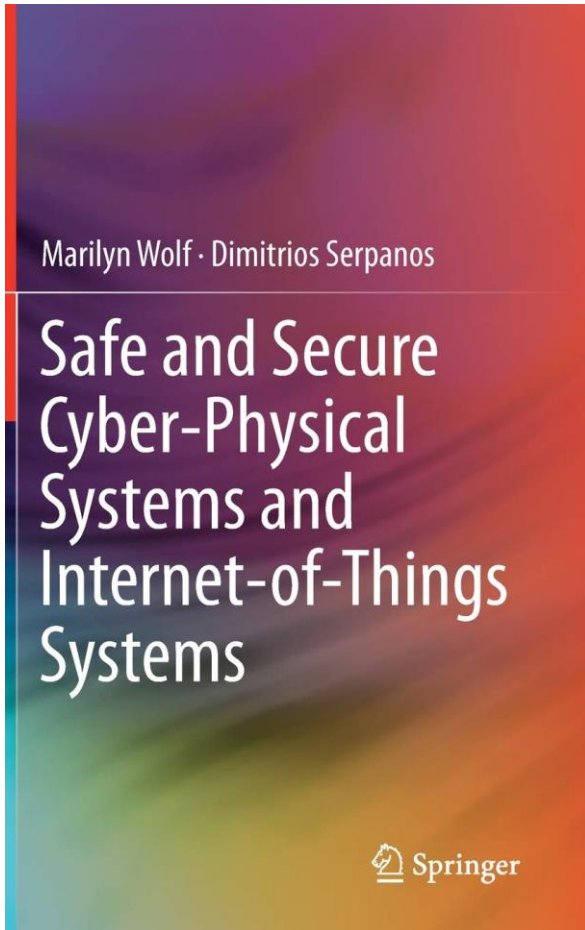


Richard Maguire: **Safety Cases and Safety Reports – Meaning, Motivation and Management**
Taylor & Francis Ltd (CRC Press), USA, 2017
ISBN 978-1-138075320

Risk Management Report [Safety Case]

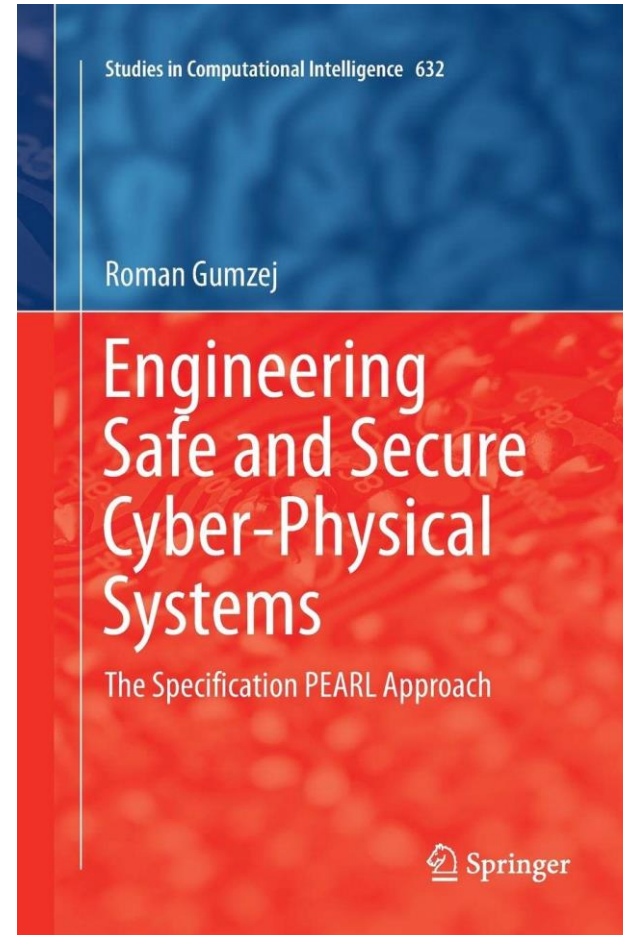


<https://www.witn.com>



Marilyn Wolf, Dimitrios Serpanos:
**Safe and Secure Cyber-Physical
Systems and Internet-of-Things
Systems**

Springer-Verlag, 1st edition 2020
ISBN 978-3-030-25807-8



Roman Gumzej:
**Engineering Safe and Secure Cyber-Physical
Systems – *The Specification PEARL Approach***

Springer-Verlag, 1st edition 2016
ISBN 978-3-319-80454-5

Part 4B





... it was a pleasure working with you !

frank.j.furrer@bluewin.ch

frank.furrer@mailbox.tu-dresden.de